

FINAL PROGRAM

SESSIONS OVERVIEW

Thursday, February 12th, 2026.

<i>Time</i>	<i>Activities</i>	<i>Place</i>
9:00-10:00	Registration	Room 154
10:00-11:00	Opening ceremony - Welcome speech	Plenary session Room 165
	Plenary session Chairmen: Dr. Tomasz Kaźmierski, Vančo Litovski	
11:00-11:30	Coffee Break	Room 154
11:30-13:00	Thematic session I: Energy Systems & Sustainable Technologies Chairman: Marko Dimitrijević	Session I Room 165
13:00-14:00	Lunch break, Registration	Room 154
14:00-15:30	Round table Chairman: Dejan Mirković	Round table Room 165
15:30-16:00	Coffee Break, Registration	Room 154
16:00-17:30	Thematic session II: Control, Signal Processing & Embedded Systems – Chairman: Slobodan Bojanić	Session II Room 165
17:30-18:30	EPLAN presentation for students' certification	online

Friday, February 13th, 2026.

<i>Time</i>	<i>Activities</i>	<i>Place</i>
9:00-10:00	Registration	Room 154
10:00-11:30	Thematic session III: Analog, RF & Integrated Circuit Design Chairman: Borisav Jovanović	Session III Room 165
11:30-12:00	Coffee Break	Room 154
12:00-14:00	Thematic session IV: AI, Computer Vision & Smart Systems Chairman: Ivan Ćirić	Session IV Room 165
14:00-15:00	Lunch break	Room 154
15:00-16:00	Thematic session V: Posters – Modeling, Optimization & Machine Learning Chairmen: Miljana Milić, Jelena Milojković	Session V Room 155

Saturday, February 14th, 2026.

<i>Time</i>	<i>Activities</i>	<i>Place</i>
11:00-11:30	Closing of the symposium	Room 165

9:00 – 10:00 Registration, room 154

Plenary session

Faculty of Electronic Engineering, University of Niš

Thursday, February 12th, 2026, 10:00 – 11:00 (local time), room 165

Chairmen: Tom Kazmierski, Vančo Litovski

1	Opening ceremony: Vladimir Ćirić, Vančo Litovski, Mark Zwolinski, Miljana Milić
2	Tom J. Kazmierski and Xuan Ji (Plenary): Feasibility of Automated Synthesis in Ultra-Low-Power Application-Specific Embedded Processor Design
3	Vančo Litovski (Plenary): The Small Systems Simulation Symposium 26 years on

11:00 – 11:30 Coffee break, room 154

Session I - Energy Systems & Sustainable Technologies

Faculty of Electronic Engineering, University of Niš

Thursday, February 12th, 2026, 11:30 – 13:00 (local time), room 165

Chairman: Marko Dimitrijević

1	Novak Radivojević, Matija Špeletić, Uroš Ilić, Andrija Petrušić, Miona Andrejević Stošović (Invited Keynote): Investigating Feature Importance of Weather and Solar Parameters for Predicting Solar Power Generation with MARS Algorithm
2	Jelena Milojković, Miljana Milić, Slobodan Bojanić, Octavio Nieto, Vančo Litovski: Comparison of the Isolated and Non Isolated SEPIC Converter
3	Duško Lukač: Integration of EPLAN into Project-Based and Self-Directed Learning for Automation Students
4	Marko Dimitrijević, Dragan Vučković, Miodrag Stojanović, Dejan Jovanović and Nenad Cvetković: Influence of LED Lamps Luminous Flux Regulation on Current Harmonics Emissions
5	Aleksandar Panić, Igor Jovanović and Dragan Mančić: Modeling of a Low-power Solar Power Plant

13:00 – 14:00 Lunch break, Registration, room 154

Round table - Fabless Business Model in Semiconductor Industry: Current State and Future Trends

Faculty of Electronic Engineering, University of Niš

Thursday, February 12th 2026, 14.00h-15.30h, room 165

Chairman: Dejan Mirković

Abstract:

This round table focuses on the fabless business model within the semiconductor industry. It will explore the current landscape and emerging trends through a discussion with some of the most hands-on professionals from the fabless semiconductor community in Serbia.

The conversation will center on industry needs regarding competencies, training, and career opportunities. The event is aimed at engineers and engineering students from both industry and academia who work, study or conduct research in a field of design, testing, and verification of Application-Specific Integrated Circuits (ASICs).

We believe the round table will be especially valuable for students considering a career in ASIC design, as seasoned colleagues with 10 to 20+ years of experience in industry and research will share firsthand insights and real-world stories.

Agenda:

- Introduction of the speakers and brief overview of speaker's career development. (25 min = 5 min per speaker)
- Current industry needs and challenges from the perspective of each speaker. (25 min = 5min per speaker)
- Place and role of next generation of ASIC engineers in context of future industry needs perceived by each of the speakers. (25 min = 5min per speaker). - Q&A. (15 min)

Speakers:

[Milan Savić, Ph.D.](#)

CEO at Navissus, Belgrade, Serbia

[Vladimir Petrović, Ph.D.](#)

Engineering Unit Manager –
GEC Serbia and Greece,
Capgemini LLC Belgrade, Serbia

[Vladimir Milovanović, Ph.D.](#)

Associate Professor, Faculty of
Engineering, University of
Kragujevac, Serbia

[Stevan Stevanović, M.Sc.](#)

Engineering Unit Director –
GEC Serbia and Greece,
Capgemini LLC Belgrade, Serbia

[Miloš Lazić, M.Sc.](#)

Layout Team Lead,
Capgemini Engineering,
Capgemini LLC Belgrade, Serbia

Note: Given timings are mere guideline. The speakers are free to extend or shorten their talk according to personal preference and vibe of the audience.

15:30 – 16:00 Coffee break, Registration, room 154

Session II - Control, Signal Processing & Embedded Systems
Faculty of Electronic Engineering, University of Niš

Thursday, February 12th, 2026, 16:00 – 17:30 (local time), room 165

Chairman: Slobodan Bojanić

1	Darko Mitić, Zoran Milić and Saša S. Nikolić: On Design of Digital Lead-Lag Compensators for Ship Roll Stabilization
2	Zoran Perić, Nikola Simić, Milan Tančić, Sofija Perić, Oliver Rhodes, Ivan Popović: Analysis of Classification Accuracy of FxP 8-bit and FP 8-bit Quantizers in GNNs
3	Zivko Kokolanski, Mare Srbinovska, Kiril Demerdziev, and Vladimir Dimchev: A Modular Virtual Environment for Teaching Advanced Topics in Measurement Science
4	Andrija Brzaković: Developing a Serial Production Optical Inspection System for Electrical Components Based on ESP32 Controller
5	Petar Stančić, Matija Špeletić, Ivana Đorđević, Stevica Cvetković, and Goran Stančić: Design of IIR Filters for Second-order Derivative Estimation

EPLAN presentation for students' certification

Faculty of Electronic Engineering, University of Niš

Thursday, February 12th, 2026, 17:30 – 18:30 (local time), online

Chairman: Duško Lukač

9:00 – 10:00 Registration, room 154

Session III - Analog, RF & Integrated Circuit Design
Faculty of Electronic Engineering, University of Niš

Friday, February 13th, 2026, 10:00 – 11.30 (local time), room 165

Chairman: Borisav Jovanović

1	Marko Andjelković, Rizwan Tariq Syed, Markus Ulbricht, Fabian Vargas, Zoran Perić, Milan Dincić, Jelena Nikolić, Aleksandra Jovanović, Dejan Cirić, Tatjana Nikolić, Goran Nikolić, Jelena Nedeljković, Sofija Perić, Nikola Vucić, Milos Krstić (Invited Keynote): An Overview of On-Chip Sensing and Analytics for Resilient Integrated Circuits
2	Nikola Ristić, Radivoje Đurić: An Autocalibrated CMOS Bandgap Reference with Improved PVT Robustness in 65nm CMOS Technology
3	Zlatica Marinković, Biljana Stošić, Marija Milijić, Dušan Marković, Girolamo Tagliapietra, and Jacopo Iannacci: Artificial neural networks assisted analysis of an RF-MEMS switch for W-band applications
4	Petar Ristić, Borisav Jovanović: The Implementation of Square Root Circuits in ASIC
5	Milan Pavlović, Andrey Shadrin: Design for Power Integrity: Decap Selection Strategies for Low Target Impedance

11:30 – 12:00 Coffee break, room 154

Session IV - AI, Computer Vision & Smart Systems
Faculty of Electronic Engineering, University of Niš

Friday, February 13th, 2026, 12:00 – 14:00 (local time), room 165

Chairman: Ivan Ćirić

1	Ivan Ćirić, Zlatan Car, Jelena Milojković (Invited Keynote): Towards Smarter Small Systems: Vision-Based AI, Edge Computing, and XAI for Next-Generation Industrial Applications
2	Nikola Ivačko, Milica Tasić, Mladen Dinčić, Anita Stanković and Ivan Ćirić: Robotic Motion Simulation with Integrated Computer Vision
3	Marina Miladinović, Miljana Milić: Detection of PCB Defects Using Deep Learning
4	Milan Stojanović: Application of Computer Vision for Detecting Electric Scooters in Vehicle Blind Spots
5	Milan Stojanović, Dejan Stevanović, Slavimir Stošović, Draženko Rajković and Stefan Skorić: Application of Modern GSM Telemetry Device for Bird Tracking
6	Srđan Đorđević, Milan Simić: Appliance Identification Using Steady State and Transient State Features

14:00 – 15:00 Lunch break, room 154

Session V - Posters – Modeling, Optimization & Machine Learning

Faculty of Electronic Engineering, University of Niš

Friday, February 13th, 2026, 15:00 – 16:00 (local time), room 155

Chairmen: **Miljana Milić, Jelena Milojković**

1	Aleksandra Dimković: Simulation and Machine Learning-Assisted Optimization of X-ray Detector Circuit for Noise Reduction
2	Dejan Mirković, Milena Stanojlović Mirković, and Vladimir Petrović: Design and Simulation of Telescopic Operational Amplifier in 180nm CMOS Technology
3	Dimitrije Nikolić: Machine Learning for Predicting Failures of Electronic Components
4	Milica Mladenović: Simulation of Degradation Processes in Lithium-Ion Batteries Using Python Modelling
5	Miljan Jeremić, Jelena Milojković: Utilizing Artificial Neural Networks to Forecast Weather and Predict Evapotranspiration Using Local Data
6	Miljana Milić and Mladen Janković: Particle Swarm Optimization for Ring Oscillators Design
7	Živojin Nikolić: Aging in RF Design: Long-Term Reliability and Performance Degradation Over Time
8	Igor Kocić, Saša S. Nikolić, Dragan Antić, Darko Mitić, Marko Milojković, Nikola Danković and Staniša Perić: Discrete Sliding Mode Control Realized with MicroLogix 1400 and S7-1200 in Industrial Environment